



Fig. 2

	+	ם ס		First electronic component					
	PCI	Chip		First elec component			VGA		
AD 31	AD 0	FRAME	DEVSEL		FG_AD 31	FG_AD 0 :	FG_FRAME	FG_DEVSEL	

Second electronic component

Fig. 3

PCI_AD 31		
PCI_AD 0	PCI	
PCI_FG_FRAME_N	£thernet Chip	,
PCI_DEVSEL_N	1	
	First electronic component	nic
PCI_AD 31		
PCI_AD 0	PCI	
PCI_FG_FRAME_N	VGA	
PCI_DEVSEL_N	CHIF	

Second electronic component

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